

Republic of the Philippines  
**TECHNICAL EDUCATION AND SKILLS DEVELOPMENT AUTHORITY**  
East Service Road, South Luzon Expressway, Taguig City

**120<sup>th</sup> TESDA BOARD MEETING**  
22 April 2020, Wednesday, 10:00 a.m.  
Through Online Teleconference Via Zoom Platform

**Resolution No. 2020 - 11**  
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**APPROVING THE RECOGNITION OF THE DIPLOMA PROGRAM IN  
ELECTRONICS ENGINEERING TECHNOLOGY OF AGUSAN DEL SUR SCHOOL  
OF ARTS AND TRADES (ASSAT) IN AGUSAN DEL SUR**

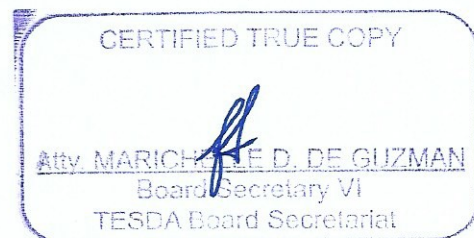
**WHEREAS**, Section 14 (b) of Republic Act No. 7796 mandates that TESDA shall develop and establish a national system of skills standardization, testing and certification and conduct research and development on various occupational areas in order to recommend policies, rules and regulations for effective and efficient skills standardization, testing and certification system in the country;

**WHEREAS**, on 16 January 2018, Republic Act No. 10968 entitled "An Act Institutionalizing the PQF, Establishing the PQF-National Coordinating Council (NCC) and Appropriating Funds Therefor" was enacted, to institutionalize the Philippine Qualifications Framework (PQF), which sets the standards for qualification outcomes;

**WHEREAS**, the Philippines has been engaged in the process of referencing the PQF with the ASEAN Qualifications Reference Framework (AQRf), which is a regional framework that enables comparisons of qualifications across the ASEAN Member States (AMS) towards greater mobility of students and professionals within the ASEAN region;

**WHEREAS**, as part of the referencing process stipulated in the AQRf Referencing guidelines, the Philippines has to undergo the procedure of including Qualifications in a national database of quality-assured qualifications, known as the "Philippine Qualifications Register (PhQuaR)";

**WHEREAS**, TESDA developed a Diploma level qualifications aligned with the descriptors of the PQF, benchmarked with ASEAN Mutual Recognition Arrangements (MRAs), among others, and issued Circular No. 38 series of 2017 entitled:



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"Implementing Guidelines for the Program Under PQF Level V (Diploma) Pilot." The Diploma programs covered by this Circular are described by the agreed/developed seventeen (17) Program Learning Outcomes (PLOs);

**WHEREAS**, the Ladderized Education Act or the Republic Act No. 10647 institutionalizes the "...ladderized interface between technical-vocational education and training and higher education to open the pathways of opportunities for career and educational progression of students and workers, create a seamless and borderless system of education, empower students and workers to exercise options or choose when to enter and exit in the educational ladder, and provide job platforms at every exit as well as the opportunity to earn income;"

**WHEREAS**, at the interface of TESDA and CHED at PQF Level V, the embedded programs include TVET Diploma and Bachelor's Degree Program specifically in: Diploma in Electronics Engineering Technology and Bachelor of Science in Electrical Engineering or Bachelor of Technical-Vocational Teacher Education (BTVTEd) which Agusan del Sur School of Arts and Trades (ASSAT) has signed into a Memorandum of Agreement (MOA) with Surigao State College of Technology on 7 May 2007 through credit transfer system;

**WHEREAS**, the Diploma in Electronics Engineering Technology and its curriculum were co-developed by ASSAT and by their respective industry partners from electrical, electronics and mechatronics sectors in consonance with their work processes;

**WHEREAS**, the Diploma in Electronic Engineering Technology covers subjects like Electrical circuits, elements of electrical engineering, digital electronics, embedded



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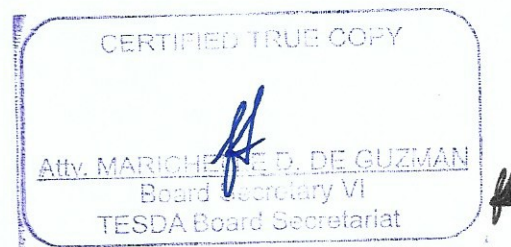
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systems, electrical and electronics machines and measurements, electrical power, switchgear, and protection. The topics covered under diploma in electronics engineering technology are semiconductor electronic components like resistors, inductors and capacitors (RLC), advanced engineering technology, Uni-Junction Transistors (UJT). Further, digital electronics covers microprocessor, microcontroller, electronic circuits, integrated circuits (ICs), operation amplifier (Op-Amp), Bipolar junction transistors, MOSFETs, JFETs, Flip Flops, diodes, transistors, and concepts of control system, power transmission systems, motors and robotics, transformers, analog and digital electronics, radio engineering and telecommunications, C-programming and algorithms. In the laboratory courses of diploma in electronics engineering, students will get to learn how to operate CROs (Cathode Ray Oscilloscopes), Function or signal generator, Breadboards, and how to design electrical circuits on printed circuit boards (PCBs);

**WHEREAS**, the Electronics Engineering Technology includes a diverse range of sub- disciplines, such as applied design, electronics control, embedded systems, control systems, and instrumentation. The program covers various underpinning knowledge and, skills especially which are: (1) relevant provisions of RA 9292 (Electronics Engineering Act) and the Philippine Electronics Code; (2) safety and health requirements; and (3) supervisory skills. The Diploma Program have entry and exit points at the end of each PQF level qualification to ensure that learners who will take programs have opportunity to land for jobs and are given credit should they pursue a higher education program. The duration of the Diploma Program is three (3) years;

**WHEREAS**, during the 113<sup>th</sup> TESDA Board meeting on 9 July 2019, a nine (9) point guidelines was agreed as a requirement for the recognition of Diploma Program, to wit:



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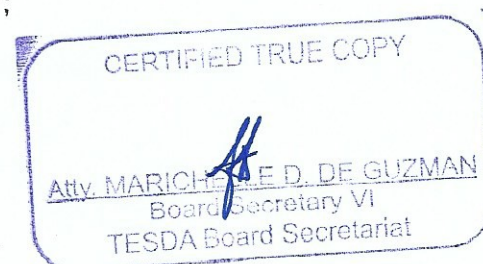
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1. Description of the development process;
2. Partnership arrangements including Enterprise-based Training modalities and credit transfer system with higher education institutions;
3. Assessment arrangements;
4. Program content including embedment of Training Regulations;
5. Curriculum content and duration;
6. Trainees entry requirement;
7. Trainers Qualifications;
8. Alignment with the 17 Program Learning Outcome (PLOs) and compliance with the 82 Performance Indicators (PIs) in the evaluation of Diploma programs;
9. Establishment of a Regional Advisory Council for Diploma Programs responsible for monitoring, evaluating, reviewing and amending as necessary to make it more relevant to the needs of the area. Members of the Regional Advisory Council shall be composed of the following:
  - a. School President/CEO/Administrator;
  - b. TESDA Regional Director;
  - c. Representatives from the Industry Sector (Employer and Labor);
  - d. Representative from the Local Government Unit (Ex-Officio); and
  - e. TESDA Board Member (Ex-Officio)

**WHEREAS**, on the same meeting, the TESDA Board agreed to apply Sanctions and Penalties on the Recognition of the Diploma programs contained in TESDA Circular No. 23, series of 2019 (Amendment of Section XVII- Sanctions and Penalties of the Amended Omnibus Guidelines on Program Registration under the Unified TVET Program Registration and Accreditation System (UTPRAS) issued on 4 March 2019;

**WHEREAS**, the curricula developed by the training institution are aligned with the PQF level V descriptors and the 17 PLOs and course outcome, based on above-mentioned TESDA Circular No. 38 series of 2017;



*[Handwritten mark]*

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**WHEREAS**, the trainee entry requirements of the mentioned Diploma program are graduates of Senior High School;

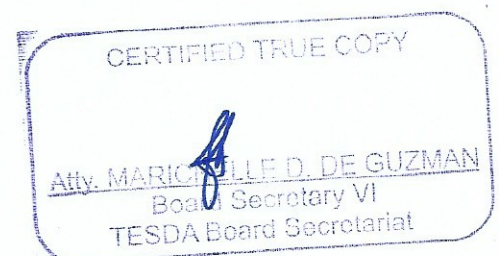
**WHEREAS**, the trainer's qualifications of the mentioned Diploma must be a licensed professional in the related field; Master's Degree holder in Education or related field; graduate of engineering programs; Technical Teacher Education, and Secondary Education, and must be a holder of National Certificate (NC) for the laboratory;

**WHEREAS**, during the 119<sup>th</sup> Standards-Setting and Systems Development (SSSD) Committee Meeting held on 10 March 2020, the Committee deliberated upon and favorably recommended to the TESDA Board the recognition of the Diploma Program in Electronics Engineering Technology;

**WHEREAS**, during the 120<sup>th</sup> TESDA Board Meeting on 22 April 2020 at 10:00 a.m., the TESDA Board considered and approved the recognition of the Diploma Program in Electronics Engineering Technology;

**NOW, THEREFORE, BE IT RESOLVED AS IT IS HEREBY RESOLVED**, that the TESDA Board in its meeting today, 22 April 2020, Tuesday at 10:00 a.m., approves and promulgates the aforementioned Diploma Program;

**BE IT RESOLVED, FINALLY, that:**



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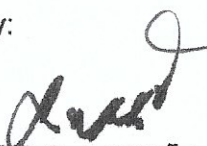
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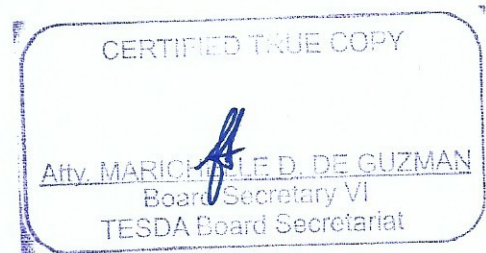
- (1) Copies of this Resolution be published in a newspaper of general circulation, and disseminated to all concerned, and the same shall be effective fifteen (15) days after publication;
- (2) A Certificate of Completion for Diploma in Electronics Engineering Technology shall be awarded by the education and training provider to students who completed the program;
- (3) Recognition of Diploma program shall ensure that all subsequent rules and regulations of TESDA pertaining to the Diploma program shall be complied with; and
- (4) Any grantee and agent/representatives shall comply with all the existing laws, rules, regulations, policies, guidelines and circulars of TESDA;
- (5) The Diploma Program shall be subject for policy review by the TESDA Board on an annual basis;

Adopted this 22<sup>nd</sup> day of April 2020.

  
**Atty. MARICHELE D. DE GUZMAN**  
TESDA Board Secretary

Attested by:

  
**SEC. ISIDRO S. LAPEÑA, PhD., CSEE**  
Designated Chairperson, TESDA Board  
Director General, TESDA



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*(Original Signed)*

**USEC. RENATO L. EBARLE**  
Department of Labor and Employment

*(Original Signed)*

**USEC. DIOSDADO M. SAN ANTONIO**  
Department of Education

**USEC. RAFAELITA M. ALDABA**  
Department of Trade and Industry

*(Original Signed)*

**USEC. ARIEL T. CAYANAN**  
Department of Agriculture

*(Original Signed)*

**USEC. EPIMACO V. DENSING**  
Department of Interior & Local Government

*(Original Signed)*

**USEC. BRENDA L. NAZARETH-MANZANO**  
Department of Science & Technology

**COMM. RONALD L. ADAMAT**  
Commission on Higher Education

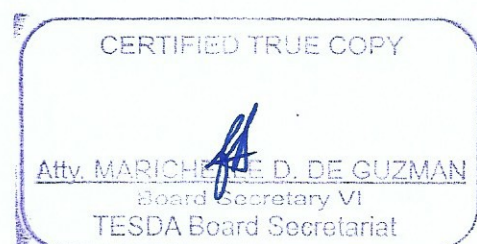
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**MR. ISIDRO ANTONIO C. ASPER**  
Board Member, Labor Sector

**ATTY. BAYANI G. DIWA**  
Board Member, Labor Sector

*(Original Signed)*

**MR. RENE LUIS M TADLE**  
Board Member, Labor Sector



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*(Original Signed)*  
**MR. RAMON R. DE LEON**  
Board Member, Labor Sector

*(Original Signed)*  
**MR. ROGELIO J. CHAVEZ, JR.**  
Board Member, Labor Sector

*(Original Signed)*  
**DR. LEONIDA BAYANI-ORTIZ**  
Board Member, Employer Sector

*(Original Signed)*  
**PROF. RANDOLPH I. NONATO**  
Board Member, Employer Sector

*(Original Signed)*  
**MS. MARY G. NG**  
Board Member, Business & Investment  
Sector



*ff*